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(((((finfet fin adj2 (fet mosfet mos transistor))
)) and ((SRAM static adj3 memory static adj RAM)
))) (((contact plug interconnect1$4 connect1$4 via) near6 ((Dl bitline bit adj line readline read adj line sense adj line .
))) with (((finfet fin adj2 (fet mosfet mos transistor))
)) fin) and (((finfet fin adj2 (fet mosfet mos transistor))
))) and gate) and (nm nanometer nano adj meter angstrom

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At 2004

	U	T	Inventor	Document	Issue	F	TIQs	Current	Current	XA	Revised	S	C	P	F	F	Image	Doc	F
1	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Yeo, Yee-Chia	US 20040009	20040		CMOS SRAM cell configured using multiple-g	257/208				<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>		US 200400	
2	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Fried, David	US 20030010	20030	2	Finfet SRAM cell using low mobility plane to	257/401	257/350			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>		US 200301	
3	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Nowak, Edw	US 6794718	20040	1	High mobility crystalline planes in double-gat	257/347	438/149			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>		US 679471	
4	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Fried, David	US 6664562	20031	1	Fin memory cell and method of fabrication	257/308	257/310			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>		US 666456	
5	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Hill, Wiley E	US 6797406	20040	2	Systems and methods for forming dense n-ch	438/164	438/302			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>		US 679740	
6	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Krivokapic, Z	US 6765303	20040		FinFET-based SRAM cell	257/25				<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>		US 676530	
7	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Grupp, Daniel	US 20040014	20040		Insulated gate field effect transistor having p	438/197	438/151			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>		US 200401	
8	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Nowak, Edw	US 20040011	20040		DENSE DUAL-PLANE DEVICES	257/204	257/208			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>		US 200400	
9	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Grupp, Daniel	US 20040002	20040		Insulated gate field effect transistor having p	257/330	257/E21.35			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>		US 200400	
10	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Fried, David	US 20030019	20031		FIN MEMORY CELL AND METHOD OF FABRI	257/200				<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>		US 200301	